## Scalable, High Performance Ethernet Forwarding on x86 Platforms Dong Zhou, Bin Fan, Hyeontaek Lim, David G. Andersen, Michael Kaminsky\* (CMU, \*Intel Labs)

## GOAL

- Single-node software switch, with
  - > One billion entries in the forwarding table
  - **10Gbps line speed**
  - > 4-8 ports support
- Motivation
  - > Content-centric networks

## **CURRENT TECHNIQUES**

- High-speed memories (e.g. TCAM)
  - > Small size severely limits scalability
- Memory efficient but approximate solutions
  - > Induce path stretch
- Prior hashing schemes
  - > Either memory inefficient and/or have unacceptable

- **Ever-larger layer-2 networks** >
- Push the limits of hardware

**lookup performance to handle collision** 

## **SOLUTION: INTEL DPDK + OPTIMIZED CUCKOO HASHING**

- Intel Data Plan Development Kit (DPDK): high-throughput packet I/O to user-space



