



# ISTC CLOUD COMPUTING RETREAT

September 3 - 5

## AGENDA

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### Wednesday, September 3, 2014

#### Optional Event - Pre-retreat dinner

Hillsboro, OR, Restaurant TBD

6:00 pm – 6:30 pm Sign-In / Reception

6:30 pm – 8:00 pm Dinner

### Thursday, September 4, 2014

#### Event Location

JF1 Presentation Room, Intel, 2111 NE 25th Ave, Hillsboro, Oregon

8:30 am – 8:55 am Continental breakfast / Registration

8:55 am – 9:00 am Welcome  
**Jeff Parkhurst** (Intel, Program Director for ISTC-CC)

9:00 am – 9:15 am Opening Remarks  
**Rich Uhlig** (Intel Labs, Executive Sponsor for ISTC-CC, Intel Fellow)

9:15 am – 10:00 am Center Overview  
**Greg Ganger** (CMU-PI) / **Phil Gibbons** (Intel-PI)

10:00 am – 11:15 am Research Talks session #1

**Margaret Martonosi** (Princeton)

“Hardware-Software Interface Issues in Heterogeneous Systems: Design, Verification, and Programming”

**Ada Gavrilovska** (Georgia Tech)

“Personal Clouds: Sharing and Integrating Networked Resources to Enhance End User Experiences”

**Dong Zhou** (CMU grad student) & **Ren Wang** (Intel Labs, SSR/NPL)

“Cuckoo Hashing: From ISTC-CC to Intel’s DPDK”

11:15 am – 11:40 am *Break*

11:40 am – 12:30 pm Research Talks session #2

**M. Satyanarayanan (Satya)** (CMU)

“Towards Wearable Cognitive Assistance”

**Haoyuan (HY) Li** (UC Berkeley grad student)

“Tachyon: A Reliable Memory Centric Storage for Big Data Analytics”

12:30 pm – 12:45 pm Poster session #1 Previews (30 seconds each)

Chaired by **Babu Pillai** (Intel Labs, ISTC-CC, Research Scientist)

12:45 pm – 2:00 pm Buffet Lunch / Poster session #1

2:00 pm – 2:15 pm *Break*

2:15 pm – 3:30 pm Research Talks session #3

**Yucheng Low** (GraphLab, Chief Architect, ISTC-CC alum)  
“What’s New with GraphLab”

**Ion Stoica** (UC Berkeley)  
“Spark: What’s New and What’s Next”

**Alex Smola** (CMU)  
“Scaling Distributed Machine Learning with the Parameter Server”

3:30 pm – 4:00 pm *Break / Group Photo*

4:00 pm – 4:50 pm Research Talks session #4

**Onur Mutlu** (CMU)  
“Some New Ideas in Memory System Design for Data-Intensive Computing”

**Alexey Tumanov** (CMU grad student)  
“Scheduling Heterogeneous Resources in Cloud Datacenters”

4:50 pm – 5:05 pm Poster session #2 Previews (30 seconds each)  
Chaired by **Michael Kaminsky** (Intel Labs, ISTC-CC, Research Scientist)

5:15 pm – 8:30 pm Reception with Poster session #2 / Dinner  
JF5 Café/Patio

## Friday, November 8, 2013

### **Event Location**

JF1 Presentation Room, Intel, 2111 NE 25th Ave, Hillsboro, Oregon

8:30 am – 8:50 am Continental Breakfast

8:50 am – 9:00 am Welcome  
**Scott Hahn** (Intel Labs, Managing Sponsor for ISTC-CC, Principal Engineer)

9:00 am – 9:45 am Keynote Talk  
**Jim Blakley** (Intel Data Center Group, Visual Cloud General Manager)  
“Visual Cloud – Video delivery, Cloud Graphics and Visual Understanding”

9:45 am – 10:00 am *Break*

10:00 am – 11:40 am Research Talks session #5

**David Andersen** (CMU)  
“Using RDMA Efficiently for Key-Value Services”

**Sudha Yalamanchili** (Georgia Tech)  
“Optimizing Performance and Productivity on Heterogeneous Processors”

**Joey Gonzalez** (UC Berkeley post-doc)  
“GraphX: Unifying Data-Parallel and Graph-Parallel Analytics”

**Garth Gibson** (CMU)  
“Exploiting Bounded Staleness to Speed Up Big Data Analytics”

11:40 am – noon All-hands Brainstorming  
Moderated by **Mike Kozuch** (Intel Labs, ISTC-CC, Principal Engineer)

noon Conclusion of retreat for all but BoA and Intel Stakeholders

**BoA / Intel:**

noon – 2:30 pm Board of Advisors meeting (includes Lunch)

*2:30 pm – 2:45 pm Break*

2:45 pm – 3:30 pm Intel-only Strategy meeting (All Intel stakeholders invited)